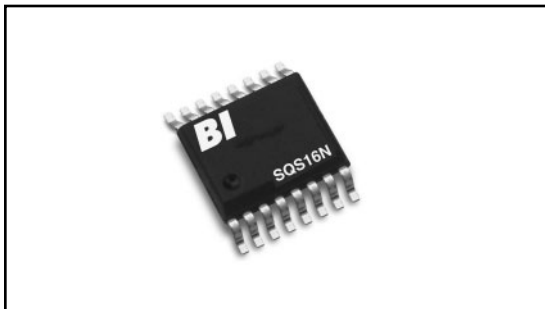


NICHROME ON SILICON

NTL Termination Circuit

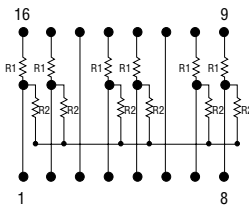
Thin Film Resistor Networks

NEW PRODUCT



SCHEMATIC

NTL (NMOS Transceiver Logic)
Termination Network
16 Pin



4

ELECTRICAL

Standard Resistance Range, Ohms	22 to 200
Operating Temperature Range	-55°C to +125°C
Interlead Capacitance	<2pF
Insulation Resistance	≥10,000 Megohms
Noise, Maximum (MIL-STD-202, Method 308)	-25dB
Maximum Operating Voltage	100Vdc or √PR

ENVIRONMENTAL

Thermal Shock plus Power Conditioning, Maximum	ΔR 0.1%
Moisture Resistance, Maximum	ΔR 0.1%
Mechanical Shock, Maximum	ΔR 0.1%
High Temperature Exposure, Maximum	ΔR 0.1%
Marking Permanency	per MIL-STD-202, Method 215
Flammability	UL-94V-0 Rated
Storage Temperature Range	-55°C to +125°C

Specifications subject to change without notice.

MECHANICAL

Lead Plating	80/20 Tin Lead
Lead Material	Copper Alloy
Lead Configuration	Gull Wing
Lead Coplanarity	0.004" (0.102mm)
Substrate Material	Silicon
Resistor Material	Passivated Nichrome
Body Material	Molded Epoxy

TOLERANCES

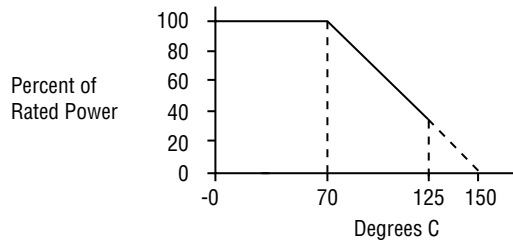
Absolute Resistance Tolerances at 25°C	1.0%
Ratio Tolerances at 25°C	1.0%
Temperature Coefficient of Resistance	±50ppm/°C
Temperature Coefficient of Resistance, Tracking	±5ppm/°C

PACKAGE POWER, WATTS @ 70°C, MAX.

QSOP	SOIC (NARROW)
16	16
0.75	1.0

Power per resistor @ 70°C, Max. is 100mW, not to exceed package power.

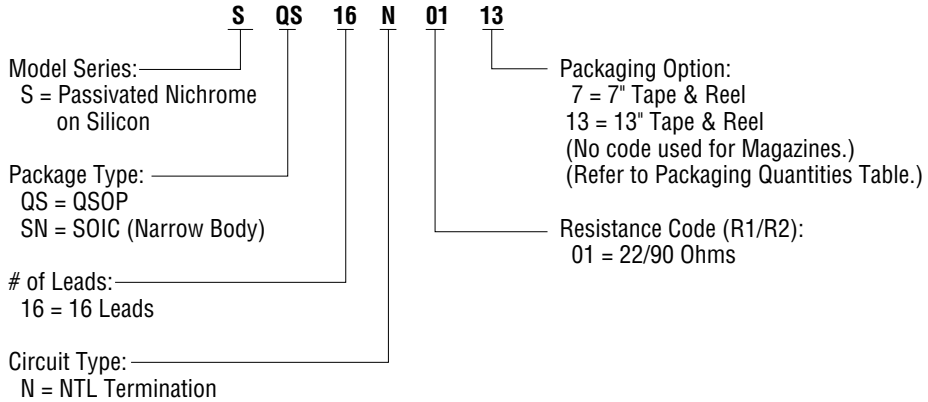
POWER DERATING CURVE



OUTLINE DIMENSIONS

Refer to standard package Outline Dimensions at the end of the Model NiCr on Si section.

ORDERING INFORMATION



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CUSTOM SOLUTIONS

Networks designed to meet your specific electrical and packaging requirements are available.
Please contact the factory for technical assistance, price and availability.
